

Remarks

Applicant thanks the Examiner for the careful examination of this application and the clear explanation of the rejections.

The new title better indicates the claimed matter.

Claim 1 defines a process of selecting different 1149.1 TAP domain arrangements within an integrated circuit.

The process performs an 1149.1 instruction shift operation through a first 1149.1 TAP domain arrangement.

The process performs an 1149.1 instruction update operation at the end of said 1149.1 instruction shift operation.

In response to the 1149.1 instruction update operation, the process selects a second 1149.1 TAP domain arrangement that differs from the first 1149.1 TAP domain arrangement.

Note that the first two subparagraphs refer to an instruction shift operation and an instruction update operation. These limitations distinguish from data shift and data update operations. See the state diagram in Figure 2.

In contrast, US 6,000,051 to Nadeau-Dostie discloses:

"...an IEEE 1149.1 standard test method in which **test data** is shifted into and from the components at the rate of a test clock during Shift_In and Shift_Out operations, and having an Update operation and a Capture operation between the Shift_In and Shift_Out operations...the method comprising the steps of performing the Shift_In operation in all of the components concurrently at the rate of the Test Clock; performing the Update and Capture Operations in the first group of components at the rate of the Test Clock; and performing the Update and Capture Operations in the second group of components at the rate of the system Clock." (Abstract, emphasis added)

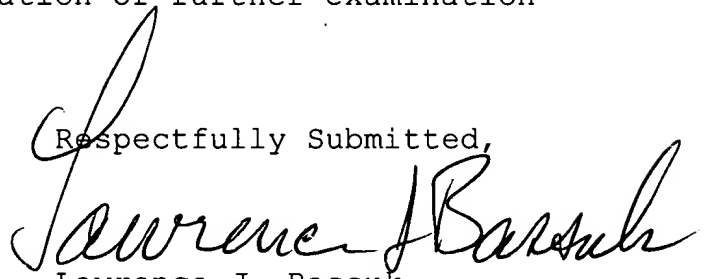
The Nadeau-Dostie patent discloses performing the (Data) Update and (Data) Capture operations between the (Data) Shift_In and (Data) Shift_Out of test data.

Further, the Nadeau-Dostie patent performs Shift_In of test data at the rate of the Test Clock, and performs the Update and Capture operations at different clock rates in different groups of components.

The Nadeau-Dostie patent fails to disclose or suggest the instruction shift and update operations, and selection limitations of present claim 1. Claim 1 is allowable.

The application is in allowable form and the claims distinguish over the cited references. Applicant respectfully requests reconsideration or further examination of this application.

Respectfully Submitted,



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